What is claimed is:

1. A data processor comprising:

an electrically erasable or programmable program memory;

a central processing unit capable of accessing the program memory; and

malfunction exclusion means for excluding a malfunction due to occurrence of an interrupt during erasing or programming of the data in the program memory.

2. A data processor comprising:

an electrically erasable or programmable program memory;

a central processing unit capable of accessing the program memory;

a storage circuit for realizing random access by the central processing unit; and

control circuit for moving a part of the storage means to a vector address area of the program memory in accordance with an interrupt request generated during erasing or programming of the data in the program memory.

3. A data processor comprising:

an electrically erasable or programmable program memory;

a central processing unit capable of accessing the program memory;

a storage circuit for realizing random access by the central processing unit; and

a control circuit for moving the vector address area of the program memory to the storage means in accordance with an interrupt request generated during erasing or programming of the data in the program memory.

4. A data processor comprising:

an electrically erasable or programmable program memory;

a central processing unit capable of accessing the program memory; and

a control logic circuit for excluding an interrupt request to the central processing unit during erasing or programming of the data in the program memory in accordance with a request for erasing or programming the data in the program memory.

5. A data processor comprising:

an electrically erasable or programmable program memory;

a central processing unit capable of accessing the program memory; and

a control logic circuit for stopping the

processing for erasing or programming the data in the program memory in response to an interrupt request generated to the central processing unit during erasing or programming of the data in the program memory in accordance with a request for erasing or programming the data in the program memory.